





Course Specifications

Course Title:	Computer Organization and Architecture	
Course Code:	CSI 323	
Program:	Computer Science and Information	
Department:	Computer Science and Information	
College:	College of Science at Az Zulfi	
Institution:	Majmaah University	



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A. Course Identification

1. Credit hours: (3) (2 Lec + 2 lab)		
2. Course type		
a.UniversityCollegeDepartmentOthers		
b. Required Elective		
3. Level/year at which this course is offered: 6^{th} Level -3^{rd} year		
4. Pre-requisites for this course (if any): Logic Design – CS1 223		
5. Co-requisites for this course (if any): N/A		

6. Mode of Instruction (mark all that apply)

No	Mode of Instruction	Contact Hours	Percentage
1	Traditional classroom	48	80%
2	Blended	6	10%
3	E-learning	0	0%
4	Correspondence	0	0%
5	Other	6	10%

7. Actual Learning Hours (based on academic semester)

No	Activity	Learning Hours
Conta	et Hours	
1	Lecture	30
2	Laboratory/Studio	15
3	Tutorial	15
4	Others (specify)	-
	Total	60
Other	Learning Hours*	
1	Study	15
2	Assignments	15
3	Library	15
4	Projects/Research Essays/Theses	15
5	Others (video lectures)	15
	Total	75

* The length of time that a learner takes to complete learning activities that lead to achievement of course learning outcomes, such as study time, homework assignments, projects, preparing presentations, library times



B. Course Objectives and Learning Outcomes

1. Course Description

This course provides the study of the structure, characteristics and operation of modern computer systems. It will focus on basic processor organization, Instruction Set Representation, ALU Design and Implementation, Processor Design Implementation, Memory Design and Organization, Input–Output Design and Organization, and Introduction to Multiprocessors.

2. Course Main Objective

- To understand the fundamentals concepts of computer organization and architecture.
- To provides the basic structure , characteristics and operations of modern computer systems.
- To understand the basic processor organization, data and control paths, instruction-level parallelism, pipelining, and multiple issue.
- To understand memory design and implementation of modern computer system, storage and input-output devices and their interfacing to the processor and memory.
- To understand the basics of storage and input-output devices and their interfacing to the processor and memory.

3. Course Learning Outcomes

	Aligned PLOs	
1	Knowledge:	
1.1	Students will learn about the concepts of computer organization and architecture.	a2
1.2	Students will be able to define the behavior and basic structure, characteristics and operations of modern computer systems.	a2
1.3		
1		
2	Skills :	
2.1	Student will develop and apply knowledge of computer organization and architecture to design the current and modern world computers.	b1
2.2	Student will gain experience in the design, development, and performance analysis of modern world computers.	b1
2.3	Student will gain experience in the application of fundamental Computer Science methods and algorithms in the development of modern memory organization, storage devices and microprocessor for modern world computers .	b3
2		
3	Competence:	
3.1		
3.2		
3.3		
3		

C. Course Content

No

List of Topics	Contact
List of Topics	Hours



1	Introduction (Computer Abstractions and Technology)	4	
2	Assessing and Understanding Performance	4	
3	Instruction Set Architecture	8	
4	Assemblers, Linkers, and the SPIM Simulator	8	
5	Arithmetic for Computers (ALU)	8	
6	Processor Design	12	
7 Memory Hierarchy and Operation 8		8	
8	Multiprocessor Systems	4	
9			
	Total 56		

D. Teaching and Assessment

1. Alignment of Course Learning Outcomes with Teaching Strategies and Assessment Methods

Code	Course Learning Outcomes	Teaching Strategies	Assessment Methods
1.0	Knowledge	- caching Strategies	1200000
1.1	Students will learn about the concepts of computer organization and architecture.	Lectures Lab demonstrations	Written Exam Homework
1.2	Students will be able to define the behavior and basic structure, characteristics and operations of modern computer systems.	Lab demonstrationsassignmentsCase studiesClass & LabIndividualActivitiespresentationsQuizzes	
2.0	Skills Student will develop and apply	[
2.1	knowledge of computer organization and architecture to design the current and modern world computers.		
2.2	Student will gain experience in the design, development, and performance analysis of modern world computers.	Lectures Lab demonstrations Case studies Individual	Written Exam assignments Lab Activities Quizzes
2.3	Student will gain experience in the application of fundamental Computer Science methods and algorithms in the development of modern memory organization, storage devices and microprocessor for modern world computers.	presentations Brainstorming	Quilles
3.0	Competence		
3.1			
3.2			

2. Assessment Tasks for Students

#	Assessment task*	Week Due	Percentage of Total Assessment Score
1	First written mid-term exam	6	20%
2	Second written mid-term exam	12	20%
3	Class activities, group discussions, Presentation	Every 2 weeks	5%
4	Homework + Assignments	After Every chapter	5%
5	Electronic exam	14	5%
6	Lab activities	15	5%
7	Final written exam	16	40%
8			

*Assessment task (i.e., written test, oral test, oral presentation, group project, essay, etc.)

E. Student Academic Counseling and Support

Arrangements for availability of faculty and teaching staff for individual student consultations and academic advice :

1. Arrangements for availability of faculty and teaching staff for individual student consultations and academic advice. (include amount of time teaching staff are expected to be available each week)

• 6-office hours per week in the lecturer schedule.

• The contact with students by e-mail, mobile, office telephone, website and BlackBoard.

F. Learning Resources and Facilities

1.Learning Resources

Required Textbooks	David A. Patterson, John L. Hennessy, "computer organization and design", 5 th edition Publishers, Inc., 2013	
Essential References Materials	 William Stallings, Computer Organization and Architecture: Designing for Performance, 10th Edition, Prentice Hall, 2015. M. Morris Mano, Computer System Architecture, 3rd Custom Edition, Pearson. 	
Electronic Materials	https://www.coursera.org/	
Other Learning Materials	Videos and presentations made available on BlackBoard e-Learning platform.	

2. Facilities Required

Item	Resources	
Accommodation	Classrooms with required digital aids and to support	
(Classrooms, laboratories, demonstration	Classioonis with required digital and to support	

Item	Resources	
rooms/labs, etc.)	traditional method of teaching using	
	blackboard.	
	Classrooms with proper lighting and air conditioning	
	system integrated with the sound	
	System /audio system.	
	Classroom with smart board interface, display	
	screen and a computer to aid the sessions	
Technology Resources	Smart Board with supporting software / computers	
(AV, data show, Smart Board, software,	with updated versions of software as required to	
etc.)	understand the subject concepts.	
Other Resources		
(Specify, e.g. if specific laboratory	N/A	
equipment is required, list requirements or		
attach a list)		

G. Course Quality Evaluation

Evaluation Areas/Issues	Evaluators	Evaluation Methods
Strategies for Obtaining Student Feedback on Effectiveness of Teaching	Instructor	Analysis of students' results. Observation during class work. Students' evaluations. Colleagues' evaluations. Evaluation questionnaire filled by the students. Interview a sample of students enrolled in the course to solicit their opinions
Other Strategies for Evaluation of Teaching	the Department	Self-assessment. External evaluation. Periodic review of course (the Commission of study plans).
Processes for Improvement of Teaching	the Department	Taking into account the recommendations yielded from the internal review of the course. Guidelines about teaching the course provided by the study plans commission. Department guidelines pertaining the faculty member's performance acquired using direct observation. Training and development. Workshops to improve the educational process
Processes for Verifying Standards of Student Achievement	Instructor	check marking by an independent member teaching staff of a sample of student work, periodic exchange and remarking of tests or a sample of assignments with staff at another institution. Instructors of the course working together with Head of Department to adopt a unique process of the evaluation.
Describe the planning arrangements for periodically reviewing course	Instructor	Comparison of the course to its counterparts offered in similar departments. Periodic revision of course description by



	Evaluation Areas/Issues	Evaluators	Evaluation Methods
	effectiveness and planning for		faculty member.
	improvement.		Periodic revision of course description by the study plans and schedules Commission. Update learning resources related to the course to ensure that the course is up-to-date with the developments in the field. Make use of statistical analysis of course evaluation carried out by the students to improve and develop the course. Provide an opportunity to the students to express their opinions about what is taught and receive suggestions and evaluate their effectiveness.
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Evaluation areas (e.g., Effectiveness of teaching and assessment, Extent of achievement of course learning outcomes, Quality of learning resources, etc.)

Evaluators (Students, Faculty, Program Leaders, Peer Reviewer, Others (specify) Assessment Methods (Direct, Indirect)

H. Specification Approval Data

Council / Committee	
Reference No.	
Date	